

FORM PT01449
(REV. 8-83)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICE

ATTY. DOCKET NO.

HARI.006USE

SERIAL NO.

08/771,708

APPLICANT

Eliyahou Harari et al.

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December 20, 1996

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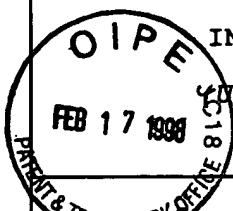
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INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

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U. S. PATENT DOCUMENTS

*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE
Ly V.	A29 4 1 8 1 9 8 0	1/1980	McCoy	365	18545	
Ly V.	A30 4 2 7 2 8 3 0	6/1981	Moench	365	45	
Ly V.	A31 4 2 8 7 5 7 0	9/1981	Stark	365	104	
Ly V.	A32 4 5 8 6 1 6 3	4/1986	Koike	365	104	
Ly V.	A33 4 6 5 3 0 2 3	3/1987	Suzuki et al.	365	104	
Ly V.	A34 4 8 4 7 8 0 8	7/1989	Kobatake	365	104	

FOREIGN PATENT DOCUMENTS

	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

Ly V.	C21	Stark, M., "Two Bits Per Cell Rom," <u>IEEE Catalog No. 81-CH1626-1</u> , Library of Congress No. 80-85186, Order No. 341, Spring Comp Con 1981, Feb. 23-26, Twenty-Second Computer Society International Conference, San Francisco, CA
Ly V.	C22	Furuyama et al., "An Experimental 2-Bit/Cell Storage Dram For Macro Cell Or Memory-On-Logic Application," pp. 4.4.1 - 4.4.4, <u>IEEE</u> , (1988)
Ly V.	C23	Horiguchi et al., "An Experimental Large-Capacity Semiconductor File Memory Using 16-Levels Cell Storage," <u>IEEE Journal Of Solid-State Circuits</u> , Vol. 23, No. 1, February 1988

EXAMINER	DATE CONSIDERED
Ly V. Harari	7/30/98

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U. S. PATENT DOCUMENTS

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LHD	A1	4	0	9	0	2	5	8	5/1978	Cricchi	365	184	
LHD	A2	4	1	4	9	2	7	0	4/1979	Cricchi et al.	365	222	
LHD	A3	4	2	7	9	0	2	4	7/1981	Schrenk	365	185.22	
LHD	A4	4	3	5	7	6	8	5	11/1982	Daniele et al.	365	185.22	

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER							DATE	COUNTRY	CLASS	SUB CLASS	TRANS.? (YES/NO)	
LHD	B1	2	0	6	1	6	5	1	B	1981	United Kingdom	—	—	
LHD	B2	5	9	3	1	1	5	8		1984	Japanese Pat. Publica.	—	—	YES

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

LHD	C1	Torelli et al., "An improved method for programming a word-erasable EEPROM," <i>Alta Frequenza</i> , Vol. LII, No. 6, pp. 487-494, Nov.-Dec. 1983
LHD	C2	Torelli et al., "Single Chip, 16-Station TV Digital Tuning System," <i>SGS-ATES Technical Note 170</i> , pp. 1-19 (1984)
LHD	C3	Torelli et al., "PLL2K-PLL Frequency Synthesizer For 30 Programs," <i>SGS-ATES Componenti Electronici SpA</i> , Technical Note 152, pp. 1-16 (1982)
LHD	C4	Scrocchi et al., "EPM32-Electronic Program Memory System For TV (32 Stations)," <i>SGS-ATES Componenti Electronici SpA</i> , Technical Note 153, pp. 1-19 (1982)
LHD	C5	"EPM 16-Electronic Program Memory (16 Stations)," <i>SGS-ATES Componenti Electronici SpA</i> , MOS Integrated Circuits/M193A-M193C-M193D, 15 pps, (November, 1981)
LHD	C6	"EPM-32 Electronic Program Memory (32 Stations)," <i>SGS-ATES Componenti Electronici SpA</i> , MOS Integrated Circuits/M293, 16 pps. (October, 1982)
LHD	C7	"Single Chip Non-Volatile Station Memory, PCM Remote Control Receiver and Display Driver," <i>SGS-ATES Componenti Electronici SpA</i> , MOS Integrated Circuits/M490-M491, 24 pps. (May, 1983)

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LHD Ly V. Hua	7/30/98

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<i>LWH</i>	A5	4	5	9	9	7	0	7	<i>7/1986</i>	Fang	365	185.02	
<i>LWH</i>	A6	4	7	1	8	0	4	1	<i>1/1988</i>	Baglee et al.	365	185.22	
<i>LWH</i>	A7	4	7	6	3	3	0	5	<i>8/1988</i>	Kuo	365	185.21	
<i>LWH</i>	A8	4	8	0	5	1	5	1	<i>2/1989</i>	Terada et al.	371	21.1	
<i>LWH</i>	A9	4	8	1	1	2	9	4	<i>3/1989</i>	Kobayashi et al.	365	185.22	
<i>LWH</i>	A10	5	0	5	3	9	9	0	<i>10/1991</i>	Kreifels et al.	711	103	
	A11	5	0	9	5	3	4	4	<i>1992</i>	Harari			

FOREIGN PATENT DOCUMENTS

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<i>LWH</i>	B3	6 2 2 5 7 6 9 9	1987	Japan			Abstr

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

<i>LWH</i>	C8	"PLL TV Microcomputer Interface," <u>SGS-ATES Componenti Electronici SpA, MOS Integrated Circuits/M290</u> , 15 pps. (June, 1982)
<i>LWH</i>	C9	Gee et al., "An Enhanced 16K E ² PROM," <u>IEEE Journal of Solid State Circuits</u> , Vol. SC-17, No. 5 (Oct. 1982)
<i>LWH</i>	C10	Torelli, "An LSI technology fully compatible EARAM cell," <u>SGS-ATES Componenti Electronici SpA</u> , 7pp. (1982)
<i>LWH</i>	C11	Caironi et al., "A TV Frequency Synthesis System With A Single Chip Microprocessor Interface Including Non Volatile Memory," <u>IEEE Transactions on Consumer Electronics</u> , Vol. CE-28, No. 3, pp. 363-71 (Aug. 1982)
<i>LWH</i>	C12	Torelli et al., "Integrating Non-Volatile Station Memory and Remote Control Receiver In A Single Chip," <u>IEEE Digest of Technical Papers</u> , Cat. No. 83CH1872-1,2pp. (June, 1983)

EXAMINER	DATE CONSIDERED
<i>Ly V. Hua</i>	<i>7/30/98</i>

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U. S. PATENT DOCUMENTS

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RWH	A12	4 3 9 3 4 7 5	7/1983	Kitagawa et al.	365	185.21	
RWH	A13	4 4 1 5 9 9 2	11/1983	Adlhoch	365	94	
RWH	A14	4 4 9 5 6 0 2	1/1985	Sheppard	365	104	

FOREIGN PATENT DOCUMENTS

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

RWH	C13	Torelli et al., "Non-Volatile Station Memory Single Chip Including Remote Control Receiver and Memory Display Driver," <u>IEEE Transactions on Consumer Electronics</u> , Vol. CE-29, No. 3, pp. 103-13 (Aug. 1983)
RWH	C14	Excerpts from a 1983 Data Book published by SGS-ATES Group, 45 pp. (Nov. 1983)
RWH	C15	Berenga et al., "E ² -PROM TV Synthesizer," <u>IEEE Digest of Technical Papers</u> , Cat. No. 78CH1298-9SSC, 4 pp. (Feb. 1978)
RWH	C16	Klingman, "Microprocessor Systems Design," <u>Prentice-Hall, Inc.</u> , pp. 30-31 (1977)
RWH	C17	Masuoka et al., "A 256-kbit Flash E ² PROM Using Triple-Polysilicon Technology," <u>IEEE Journal of Solid-State Circuits</u> , Vol. SC-22, No. 4, (August 1987)
RWH	C18	Bleiker et al., "A Four-State EEPROM Using Floating-Gate Memory Cells," <u>IEEE Journal of Solid-State Circuits</u> , Vol. SC-22, No. 3 (June, 1987)
RWH	C19	Krick, P.J., "Three-State MNOS Fet Memory Array," <u>IBM Technical Disclosure Bulletin</u> , Vol. 18, No. 12, pps. 4192-4193 (May, 1976)

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<i>LvH</i>	A15	4 8 0 7 1 8 8	2/1989	Casagrande	365	185.25	
<i>LvH</i>	A16	4 9 5 6 8 1 6	9/1990	Atsumi et al.	365	185.22	
<i>LvH</i>	A17	4 9 9 9 8 1 3	5/1991	Ohtsuka et al.	365	185.18	

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OTHER DOCUMENTS (Including Author, Title, Date, Pertinent pages, Etc.)

<i>LvH</i>	C20	Alberts, et al., "Multi-Bit Storage Fet EROM Cell," <u>IBM Technical Disclosure Bulletin</u> , Vol. 24, No. 7A, pps. 3311-3314 (December 1981)

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